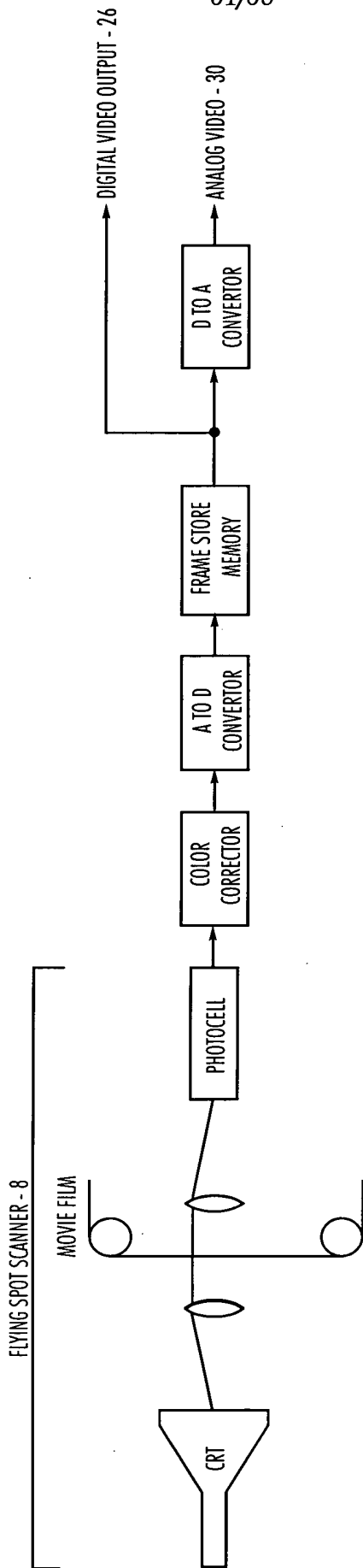


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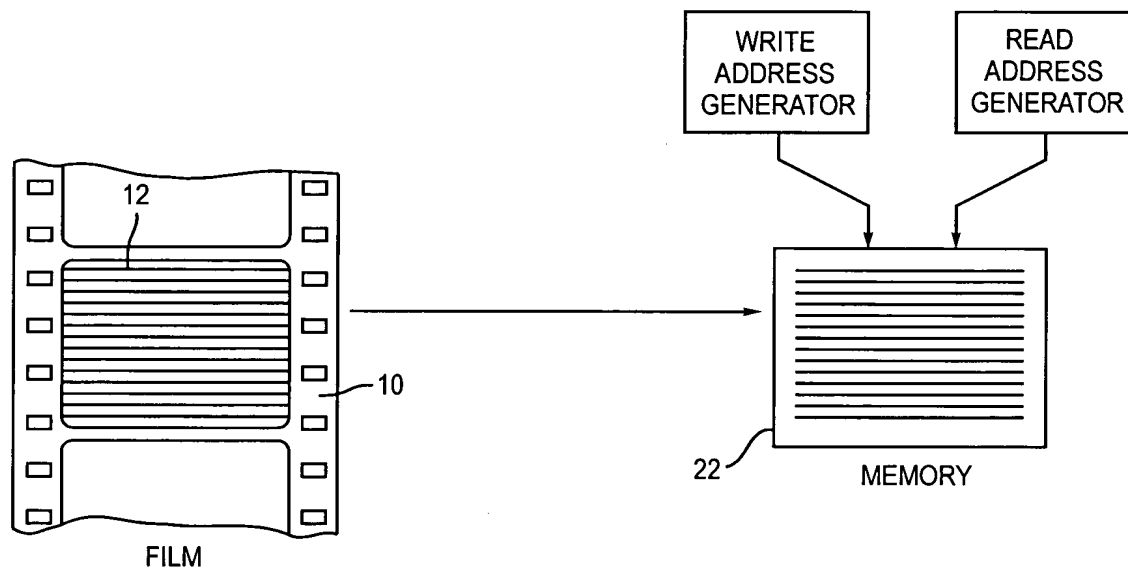


Fig. 2
PRIOR ART

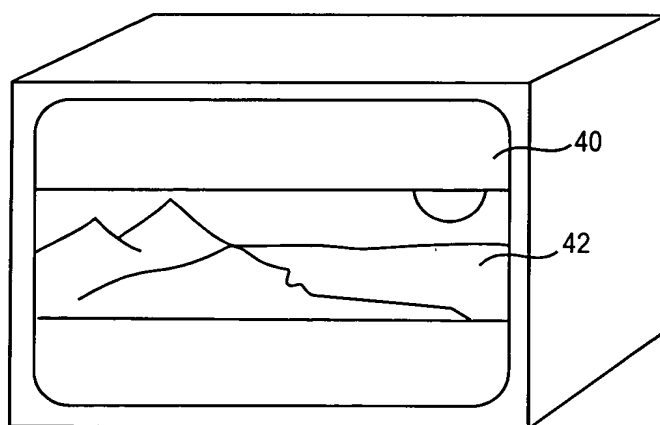


Fig. 4
PRIOR ART

03/06

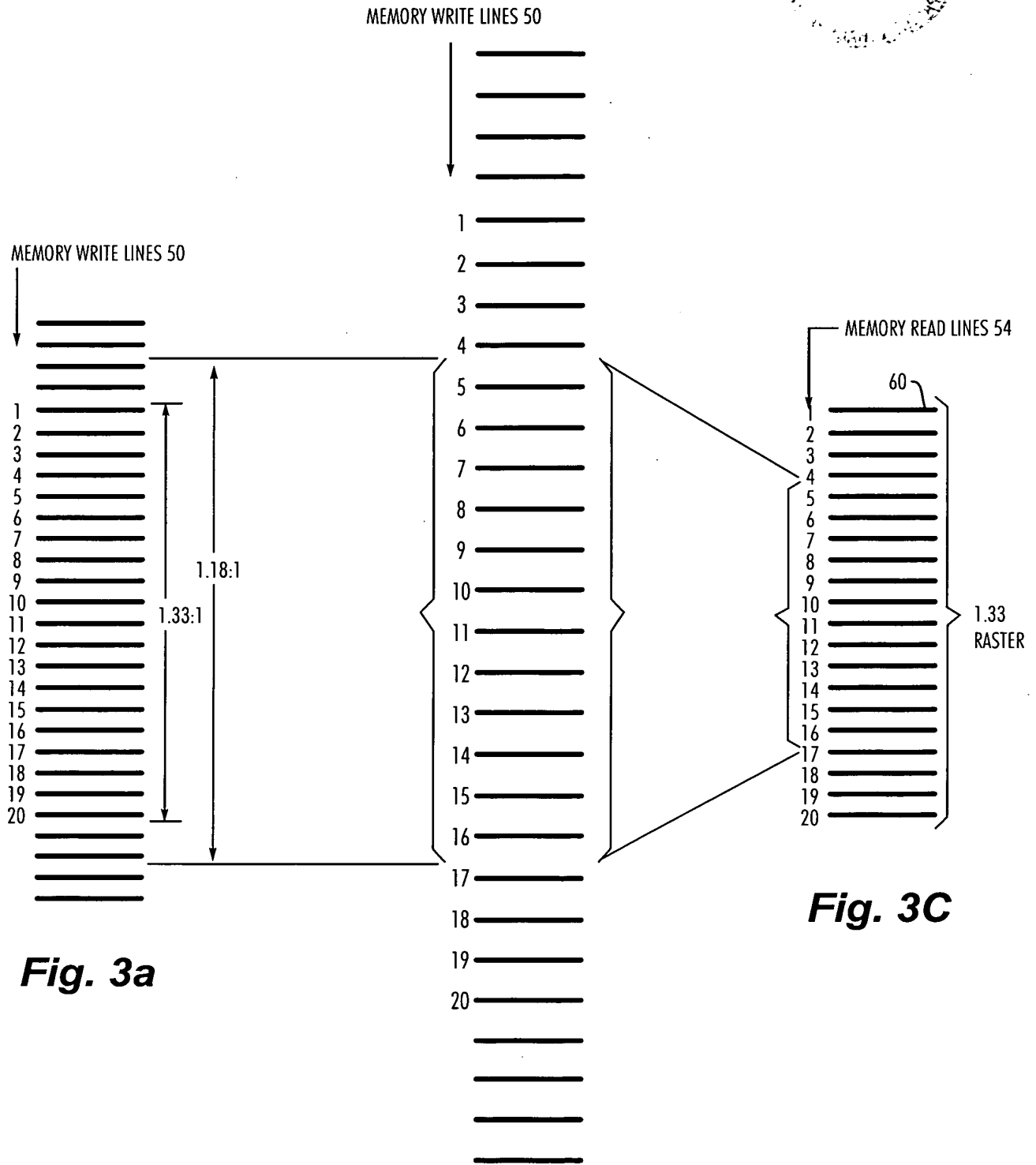


Fig. 3a

Fig. 3c

Fig. 3b

03/06

NOTE: THE 2 TO 1 VERTICAL COMPRESSION
RESULTING FROM HAVING THE "MEMORY
READ ADDRESS GENERATOR" PROCEED VERTICALLY
THROUGH THE MEMORY AT TWICE THE NORMAL,
NON-ANAMORPHIC RATE

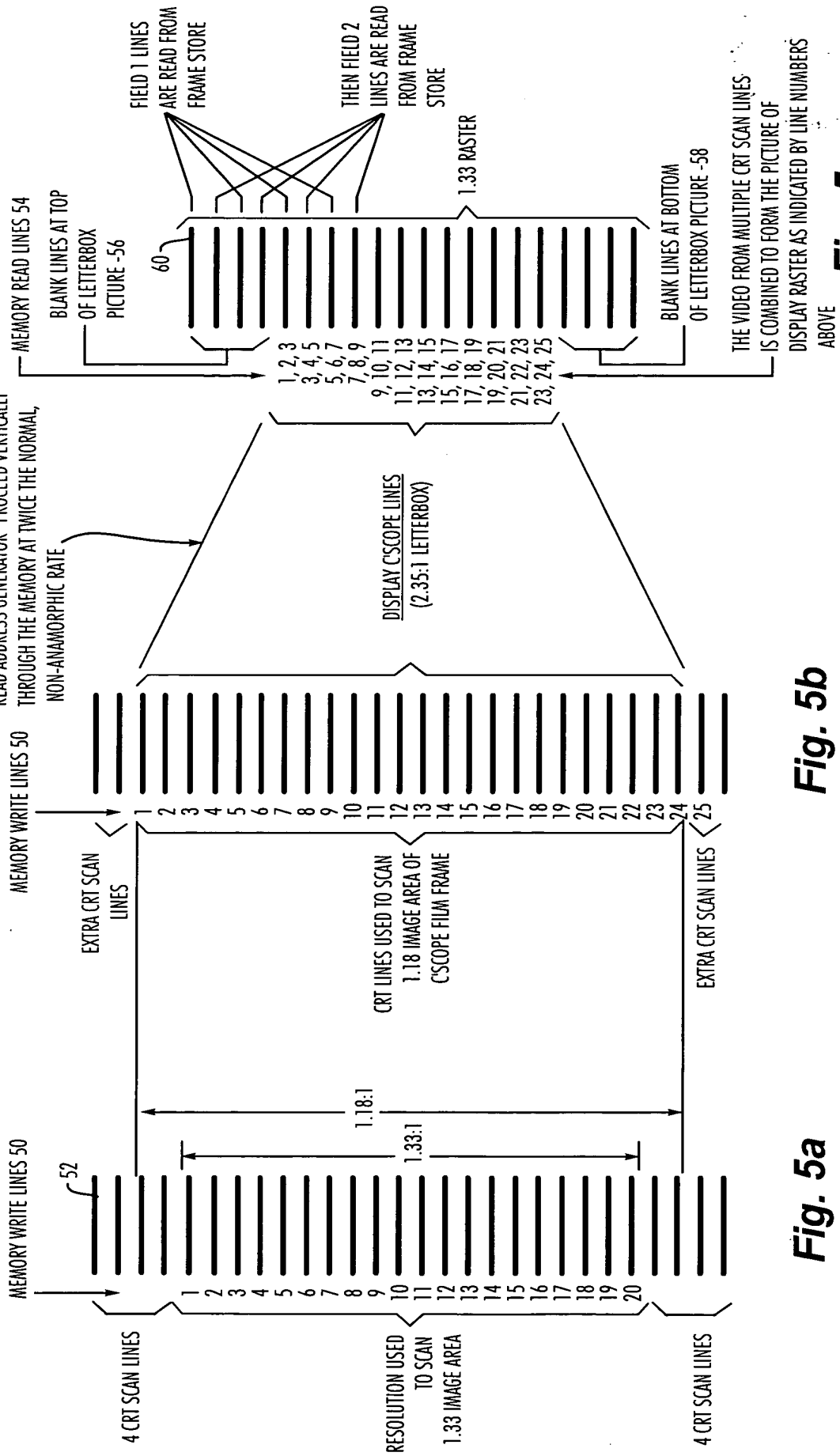


Fig. 5b

Fig. 5a

Fig. 5c

06/06

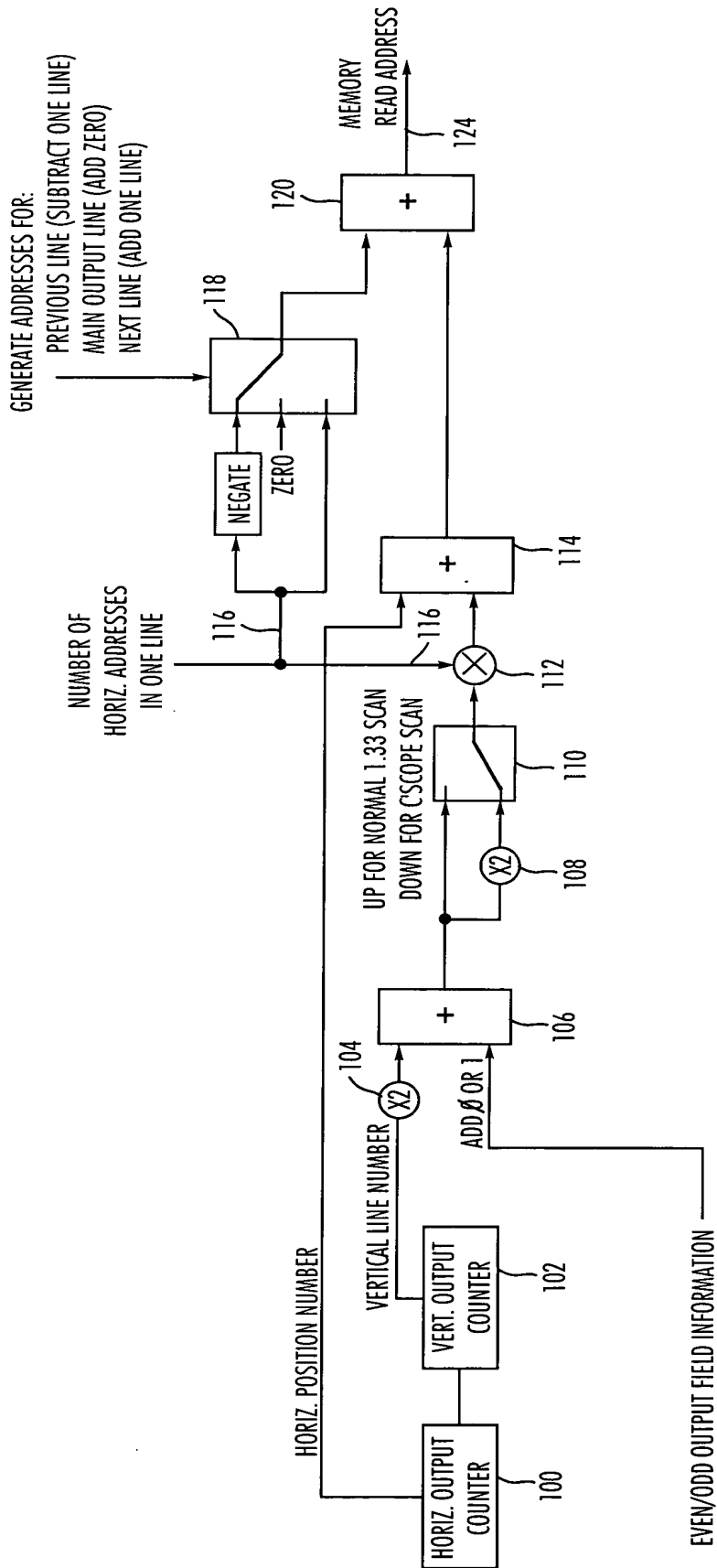


Fig. 8